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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/603,302

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Song Wu

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EXAMINER

JOSEPH, JAISON

ART UNIT

PAPER NUMBER

2611

NOTIFICATION DATE

DELIVERY MODE

10/06/2008

ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

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DETAILED ACTION

Response to Arguments

Applicant's arguments filed 8/01/2008 have been fully considered but they are not persuasive.

Regarding claims 1, 2, 4, 6 – 9, 11 and 12, Applicant argue “*Claim 1, as amended, includes "... said digital-to-analog conversion portion includes a plurality of digital-to-analog converters ...". The references of record do not show, teach, or suggest the above recited limitations of claim 1. The Sawada and Yang references do not teach how the plurality of digital to analog converters (DACs) in Yang would be substituted into the device of Sawada to obtain the device of claim 1. In Yang, the DACs are used as tail current sources for respective differential pair stages. The outputs of each DAC in Yang are not coupled to a single device such as a feed forward equalizer of claim 1. Claims 2, 4, and 6-12 depend from claim 1. Therefore, claims 1, 2, 4, and 6-12 are believed to be allowable over the references of record.*” However Examiner respectfully disagrees. Sawada et al in view of Yang et al teach said cited limitations of “... said digital-to-analog conversion portion includes a plurality of digital-to-analog converters ...”. Sawada et al teach all cited limitations except the digital to analog conversion portion include plurality of DACs. However in analogous art, Yang et al teach an filter having digital to analog conversion portion includes plurality of digital to analog converters having respective inputs coupled to said sampler and respective outputs coupled to said feed forward equalizer (see abstract and figure 6 and 12. In figure 6 of yang teach having a plurality of coefficient multipliers and the outputs of the coefficient

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multipliers are added. in figure 12 Yang teaches the coefficient multipliers are substituted with DACs.) Thus Sawada et al in view of Yang et al teach all cited limitations. Therefore Examiner maintains the rejection of claims 1, 2, 4, 6 – 9, 11 and 12. Applicant is reminded that the examiner is entitled to give broadest reasonable interpretation to the language of the claims.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JAISON JOSEPH whose telephone number is (571)272-6041. The examiner can normally be reached on M-F 9:30 - 6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chieh Fan can be reached on (571) 272-3042. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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/J. J./
Examiner, Art Unit 2611

/Chieh M Fan/
Supervisory Patent Examiner, Art Unit 2611